

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A method of manufacture of a semiconductor device, comprising the steps of:

providing an adhesive between a surface of a semiconductor chip having a plurality of electrodes on which said electrodes are provided and a surface of a substrate having a plurality of leads formed on which said leads are formed;

positioning at least one of said plurality of electrodes to oppose at least one of said plurality of leads; and

E1 applying pressure in a direction such as to make a gap between said semiconductor chip and said substrate narrower;

wherein on the surface of said substrate on which said leads are formed, in a region being at least part of a region of adherence of said semiconductor chip, a film is formed with a lower adhesion to said adhesive than a base material of said substrate, and said film is broader than each of said leads at their portions opposed to said electrodes,

wherein a region in which said adhesive is disposed includes a first region of low adhesion with said adhesive and a second region of high adhesion with said adhesive,

the area of said first region being \geq the area of said second region.

2. (Original) The method of manufacture of a semiconductor device as defined in claim 1,

wherein said adhesive is formed of an anisotropic conductive material having conductive particles dispersed in an insulating material.

3. (Original) The method of manufacture of a semiconductor device as defined in claim 1,

wherein said leads and said film are formed by etching a conductive foil adhered to said base material of said substrate.

4. (Original) The method of manufacture of a semiconductor device as defined in claim 1,

wherein a conductive foil used when forming said leads is also used to form said film.

5. (Original) The method of manufacture of a semiconductor device as defined in claim 4,

wherein said film is formed simultaneously with said leads.

6. (Original) The method of manufacture of a semiconductor device as defined in claim 1,

wherein said electrodes are provided on an extremity of said surface of said semiconductor chip; and

wherein said film is formed in a region opposing a central part of said surface of said semiconductor chip.

7. (Original) The method of manufacture of a semiconductor device as defined in claim 1,

wherein said film is formed to spread two-dimensionally, with at least one opening exposing a surface of said substrate.

8. (Original) The method of manufacture of a semiconductor device as defined in claim 1,

wherein said film is formed to project outside a region in which said semiconductor chip is adhered.

9. (Original) The method of manufacture of a semiconductor device as defined in claim 1,

wherein said film is formed to be symmetrical about a center point of a region in which said semiconductor chip is adhered.

10. (Original) The method of manufacture of a semiconductor device as defined in claim 1,

wherein said film is formed to avoid at least one of said leads.

11. (Cancelled)

12. (Currently Amended) A semiconductor device comprising:

a semiconductor chip having a plurality of electrodes;

a substrate on which is formed a plurality of leads; and

an adhesive provided between a surface of said semiconductor chip on which said electrodes are formed and a surface of said substrate on which said leads are formed, and adhering said semiconductor chip and said substrate,

wherein at least one of said plurality of electrodes and at least one of said plurality of leads are electrically connected; and

wherein on said substrate in a region including at least a part of a region opposing said semiconductor chip, a film is formed with a lower adhesion to said adhesive than a base material of said substrate, and said film is broader than each of said leads at their portions opposed to said electrodes,

wherein a region in which said adhesive is disposed includes a first region of low adhesion with said adhesive and a second region of high adhesion with said adhesive,

the area of said first region being \geq the area of said second region.

13. (Original) The semiconductor device as defined in claim 12,

wherein said adhesive is formed of an anisotropic conductive material having conductive particles dispersed in an insulating material.

14. (Original) The semiconductor device as defined in claim 12,

wherein said leads and said film are formed of the same electrically conductive material.

15. (Original) The semiconductor device as defined in claim 12,
wherein said electrodes are provided at an extremity of said surface of said
semiconductor chip; and
wherein said film is formed in a region opposing a central part of said surface
of said semiconductor chip.
16. (Original) The semiconductor device as defined in claim 12,
wherein said film is formed to spread two-dimensionally, with at least one
opening exposing a surface of said substrate.
- E1 17. (Original) The semiconductor device as defined in claim 12,
wherein said film is formed to project outside a region in which said
semiconductor chip is adhered.
18. (Original) The semiconductor device as defined in claim 12,
wherein said film is formed to be symmetrical about a center point of a region
in which said semiconductor chip is adhered.
19. (Original) The semiconductor device as defined in claim 12,
wherein said film is formed to avoid at least one of said leads.
20. (Cancelled)
21. (Original) A circuit board on which is mounted the semiconductor device as
defined in claim 12.
22. (Original) An electronic instrument having the semiconductor device as
defined in claim 12.
23. (New) The method of manufacture of a semiconductor device as defined in
claim 1,

wherein said plurality of leads includes a first lead on said substrate and a second lead which is closest to said first lead on said substrate, and a part of said film is put between said first and second leads.

24. (New) The semiconductor device as defined in claim 12,

wherein said plurality of leads includes a first lead on said substrate and a second lead which is closest to said first lead on said substrate, and a part of said film is put between said first and second leads.
